CACHE MEMORY AND ITS UTILIZATION
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Abstract

Design of cache-oblivious algorithm requires deep understanding of program’s structure and operation, and familiarity with memory architecture these prerequisites may limit the use of cache-oblivious to performance critical portion written by expert programmers is used today. To make the performance benefits of cache oblivious structure available to the average programmer. It is very necessity to design such techniques which reduce cache miss rate and achieve significant performance improvements on real programs

Keywords: Cache Oblivious, Cache miss, Cache hit; Multiplication; Transposition; Sorting; B-tree